

Attorney Docket No. 5649-877



2836
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al.

Serial No.: 09/923,670 ✓

Filed: August 7, 2001

For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND
METHODS OF FABRICATING SAME

Group Art Unit: 2836

Examiner: M. Lewis

Confirmation No.: 1538

Date: July 8, 2002

BOX NON-FEE AMENDMENT

Commissioner for Patents

Washington, DC 20231

AMENDMENT IN RESPONSE TO RESTRICTION REQUIREMENT

Sir:

This amendment is responsive to the Restriction Requirement in the Office
Action of June 18, 2002.

In the Title:

Please delete the title in all occurrences and substitute the following title
therefor: -- METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES
HAVING DIELECTRIC REGIONS PROTECTED WITH MULTI-LAYER
INSULATION STRUCTURES --.

In the Claims:

Please cancel Claims 1-41 without prejudice to the filing of a divisional
application.

REMARKS

In response to the restriction requirement in the Office Action of July 6, 2001,
Applicants hereby elect Invention II as claimed by method Claims 42-71, and cancel
Claims 1-41 without prejudice to filing of a divisional application. This election is
made without traverse, as Applicants agree that unpatentability of Invention II would
not necessarily imply unpatentability of the method of Invention I. The title has been

#7/A
T. Bell
7.26.02
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